

**Graduate Assistantship Application**  
***Department of Electrical and Computer Engineering***  
***University of New Mexico***

**Name** (Last, First, Middle): \_\_\_\_\_

**Email Address:** \_\_\_\_\_

**Degree(s) Held** (Indicate institution(s) and date(s) of graduation):  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_

**Courses Previously Taught:**  
\_\_\_\_\_  
\_\_\_\_\_  
\_\_\_\_\_

**Course Preference** (Place a check next to the courses you are interested in serving as a teaching assistant. You should select courses in which you have a strong background.):

**ECE 203 Circuit Analysis I (Drill Sessions)**  
Basic electrical elements and sources. Energy and power. Ohm's law and Kirchoff's laws. Resistive networks, node and loop analysis. Network theorems. Dynamics of first-order circuits. Sinusoidal sources and complex representations impedance, phasors, complex power. Three phase circuits.

**ECE 206/ECE 327 Electronics Laboratory (Lab)**  
Electrical Engineering Laboratory experiments in basic electrical measurements, D.C., A.C., circuits, and simple transients.

**ECE 213 Circuit Analysis II (Tutoring)**  
General transient analysis of electrical circuits. Laplace transform with applications to circuit analysis. Fourier series analysis. Introduction to state variables. The network function; impulse response; convolution; frequency response.

**ECE 238 Computer Logic Design (Lab)**  
Binary number systems. Boolean algebra. Combinational, sequential, and register transfer logic. Arithmetic/Logic unit. Memories, computer organization. Input-output. Microprocessors.

**ECE 344 Microprocessors (Lab)**  
Computers and Microprocessors: architecture, assembly language programming, input/output, and applications.

**ECE 472 Microelectronics (Lab)**  
This lecture/laboratory course is a comprehensive introduction to the major silicon microelectronics process tools and technologies. Topics include: microelectronics trends, silicon wafers fabrication, oxidation, diffusion, ion implementation, etching, lithography, CVD, metalization, packaging, yield. The lab project provides hands on experience of real MOSFET fabrication and characterization using ECE's NMOS process.

All of the above information is correct to the best of my knowledge.

\_\_\_\_\_  
Signature

\_\_\_\_\_  
Date