



TransDataToFromFPGA.vi

C:\jimp\CMPE415\415project\_labview\TransDataToFromFPGA.vi

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The front panel of the LabVIEW VI 'TransDataToFromFPGA.vi' contains the following controls and indicators:

- Data address:** A numeric display set to 378.
- Status address:** A numeric display set to 379.
- Reset pos:** A numeric display set to 2.
- DataToFPG pos:** A numeric display set to 3.
- DataValidToFPG pos:** A numeric display set to 4.
- Clk pos:** A numeric display set to 5.
- DataFromFPGA:** A numeric display set to 3.
- DataValidFromFPGA:** A numeric display set to 4.
- ErrorIn:** A control with a green indicator light and a numeric display set to 0.
- Data Out Array:** An array indicator with 8 green LEDs and a numeric display set to 0.
- Data In Array:** An array indicator with 8 green LEDs and a numeric display set to 0.
- Number of bits:** A numeric display set to 8.
- Cnter:** A numeric display set to 8.
- ErrorOut:** A control with a green indicator light and a numeric display set to 0.

A watermark for 'NATIONAL INSTRUMENTS LabVIEW Student Edition' is visible in the bottom right corner of the panel.