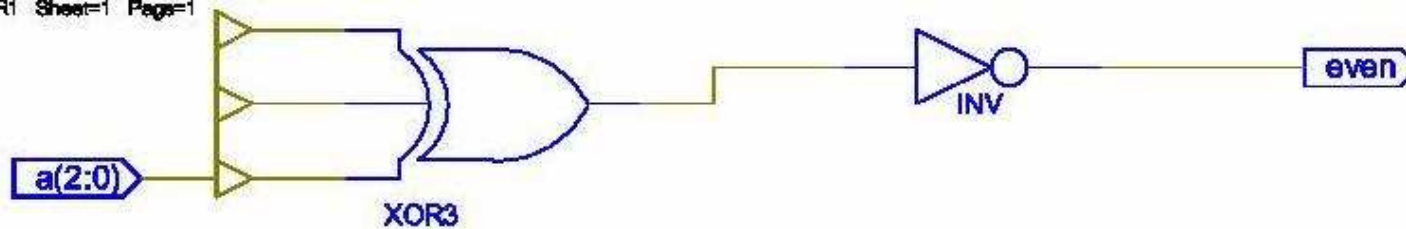


Schematics for XORs in "VHDL Overview"

For "xor_arch" and "beh1_arch"

Block=XOR1 Sheet=1 Page=1



For "beh2_arch"

Block=xor_3 Sheet=1 Page=1

