



Xilinx is seeking a qualified individual to assist us with technical responsibilities in the Albuquerque Journal Center office. This is a paid position, requiring 10 to 20 hours per week (very flexible schedule) onsite at Xilinx. Additional hours during summer may be available. Duties will include :

- Working alongside a test manager and serve as a witness to the final test of Xilinx Intellectual Property before it is delivered to Xilinx customers.
- Use of Vivado and other Xilinx tools to build and test designs in support of engineering projects.
- Creation of HDL-based designs to test and exercise Xilinx tool flows.
- Miscellaneous technical and administrative support for Albuquerque office. Previous intern duties have included :
 - Demonstration box assembly, test and calibration
 - Board level tests, programming, functional tests
 - Correlation and organization of data from working groups
 - Participation in community service projects.

Additionally, during periods of time when immediate services are not needed, we encourage our student workers to still be on-site, and they may attend to their study needs or self-paced technical learning. Access to Xilinx tools and hardware available for student use.

Period of employment : Immediate to May 30, 2020

Requirements : CS or EE student, good lab practices, with preference to those who have completed ECE238L or ECE443 or who have demonstrated ability in VHDL, Verilog, or some programming capability.

Compensation \$15 to \$20 per hour depending on education and experience level. US Citizenship required. See <https://careers.xilinx.com/jobs/3633363-engineering-intern> For more details.

Please send resume and cover letter to : frankw@xilinx.com