

Name _____
Date _____
Section _____

Lab 4 Worksheet

General Knowledge

1. What does IP stand for?
2. Why would an engineer want to use and IP core?
3. Find the missing values in the following table.

Decimal Value	Number of Bits for Two's Complement Value	Two's Complement Value	Maximum	Minimum
536	11			
	9	101111001		

Final task: Design an Adder / Subtractor Using The CORE Generator System

- Turn in a copy of your VHDL code.
- Turn in a copy of the finished test bench code.
- Turn in a copy of the simulation waveform with the radix changed to decimal.

Make sure your T.A checked and approved the output of your simulation!!