

ECE 238 Lab

Name:.....

Section:.....

Date:.....

Lab Task

In this lab, you have to implement with VHDL the circuit of Lab 1, which performs the following operation:

$$F = A'B'D + BC'D + A'BC + ACD$$

Test your design with all possible combinations of input values. Turn in your VHDL code, testbench and waveform.