

Current Simulation
Time: 6700 ns

0

dFf	1	
dvFf	0	
data_out...	1	
clk	1	
dTf	1	
reset	0	
dvTf	0	
bit_pos[4:0]	5'h1F	5'hXX 5'h00 5'h01 5'h02 5'h03 5'h04 5'h05
cur_state[2:0]	3'h0	3'hX 3'h0
next_sta...	3'h0	3'h0
mem_in[31:0]	3...	32'hXXXXXXXX 32... 32'h00000001 32'h00000005 32'h00000015
mem_out[...]	3...	
mem_writ...	0	
mem_addr...	8'h02	8'hXX
last_addr[7:0]	8'h02	8'hXX
inc_mem_...	0	



5'h06 5'h07 5'h08 5'h09 5'h0A 5'h0B 5'h0C 5'h0D 5'h0E 5'h0F 5'h10

32'h00000055 32'h00000155 32'h00000555 32'h00001555 32'h00005555
32'h00000000

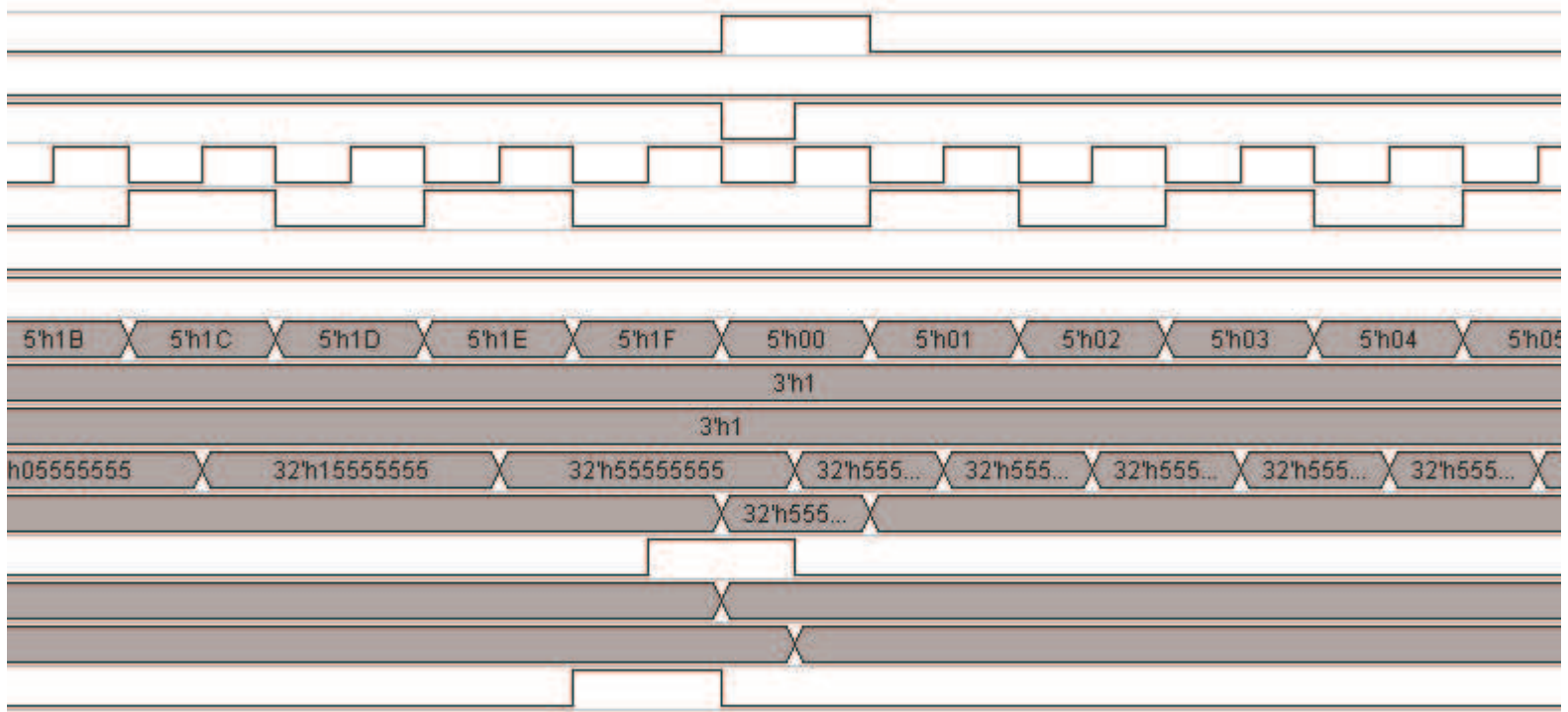
8'h00
8'h00

1

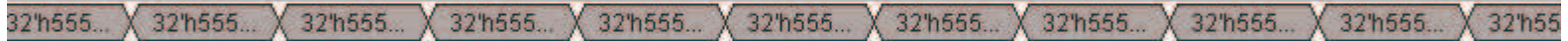


5'h11 5'h12 5'h13 5'h14 5'h15 5'h16 5'h17 5'h18 5'h19 5'h1A

32'h00015555 32'h00055555 32'h00155555 32'h00555555 32'h01555555 32'h05555555

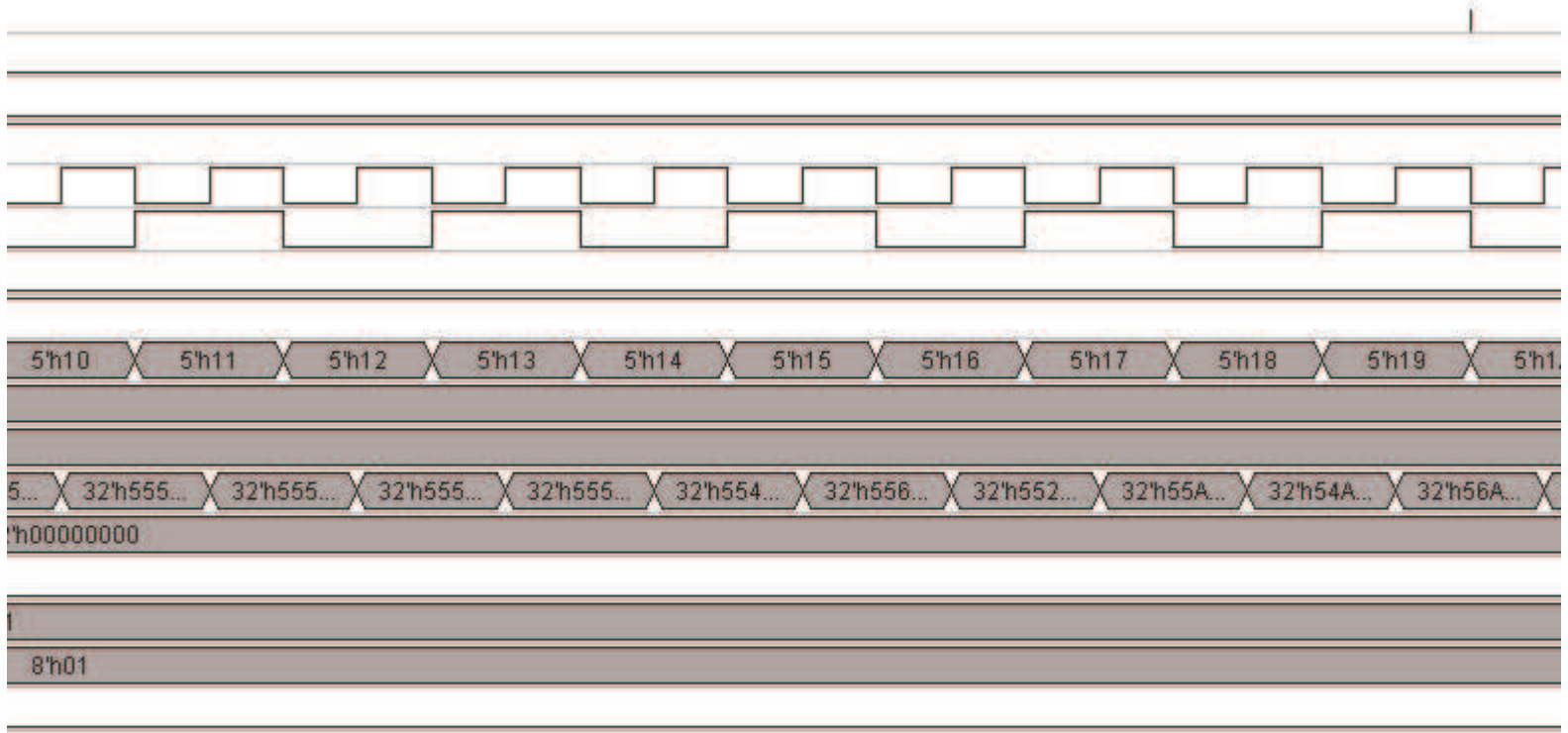


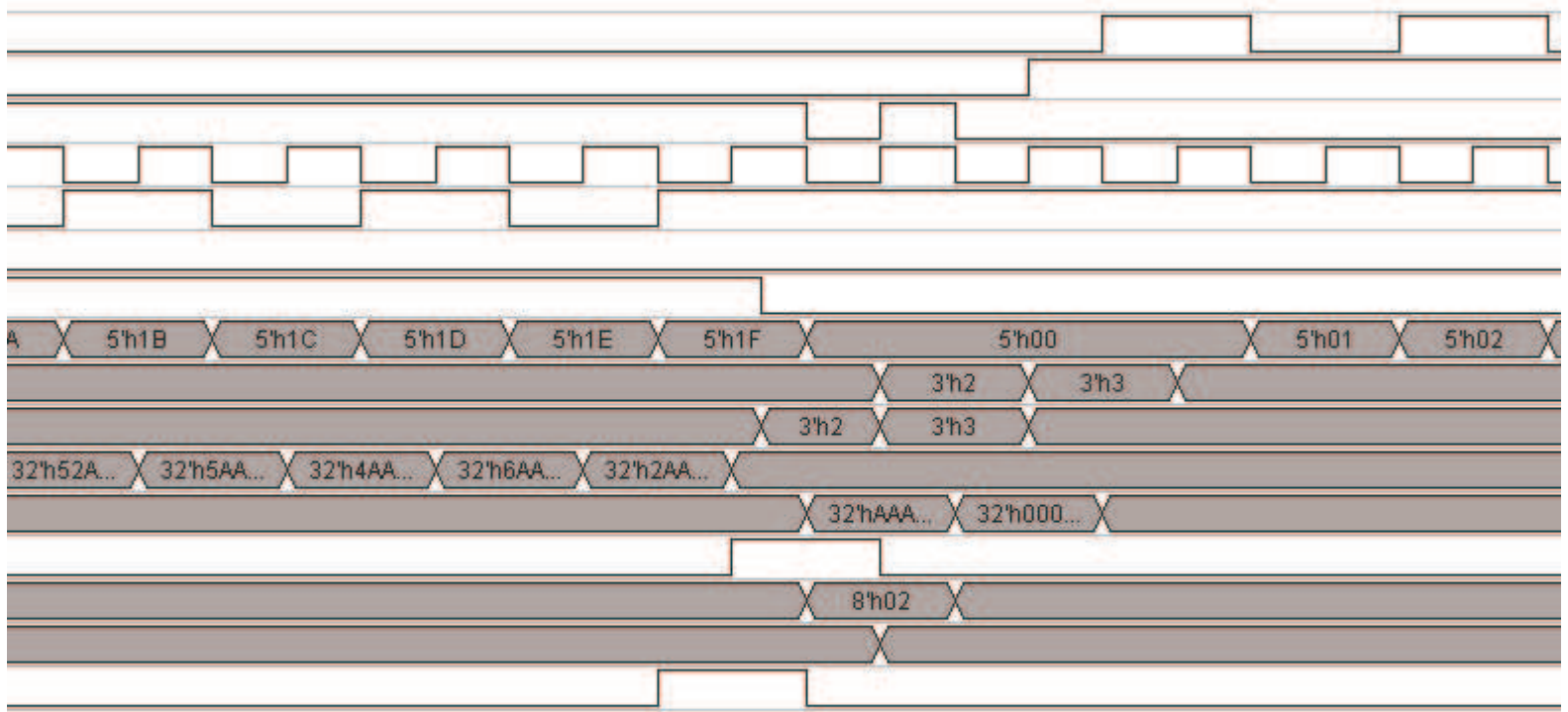
2000



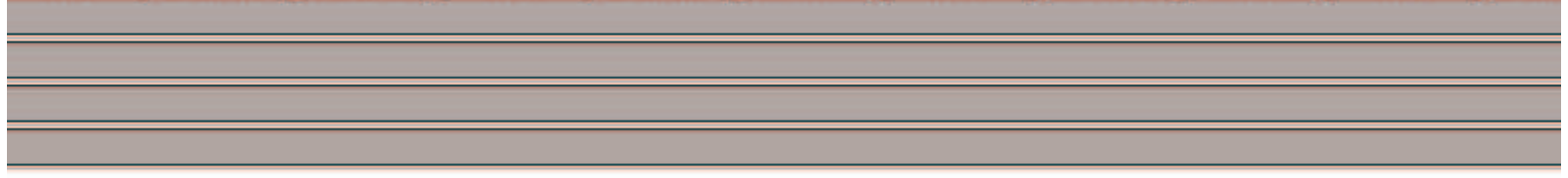
32

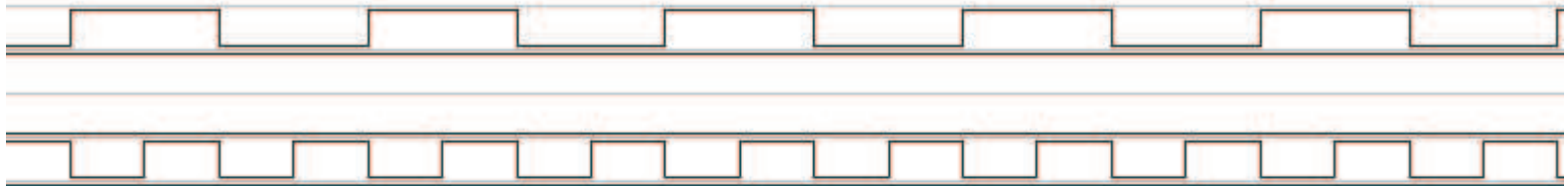
8'h0





4000

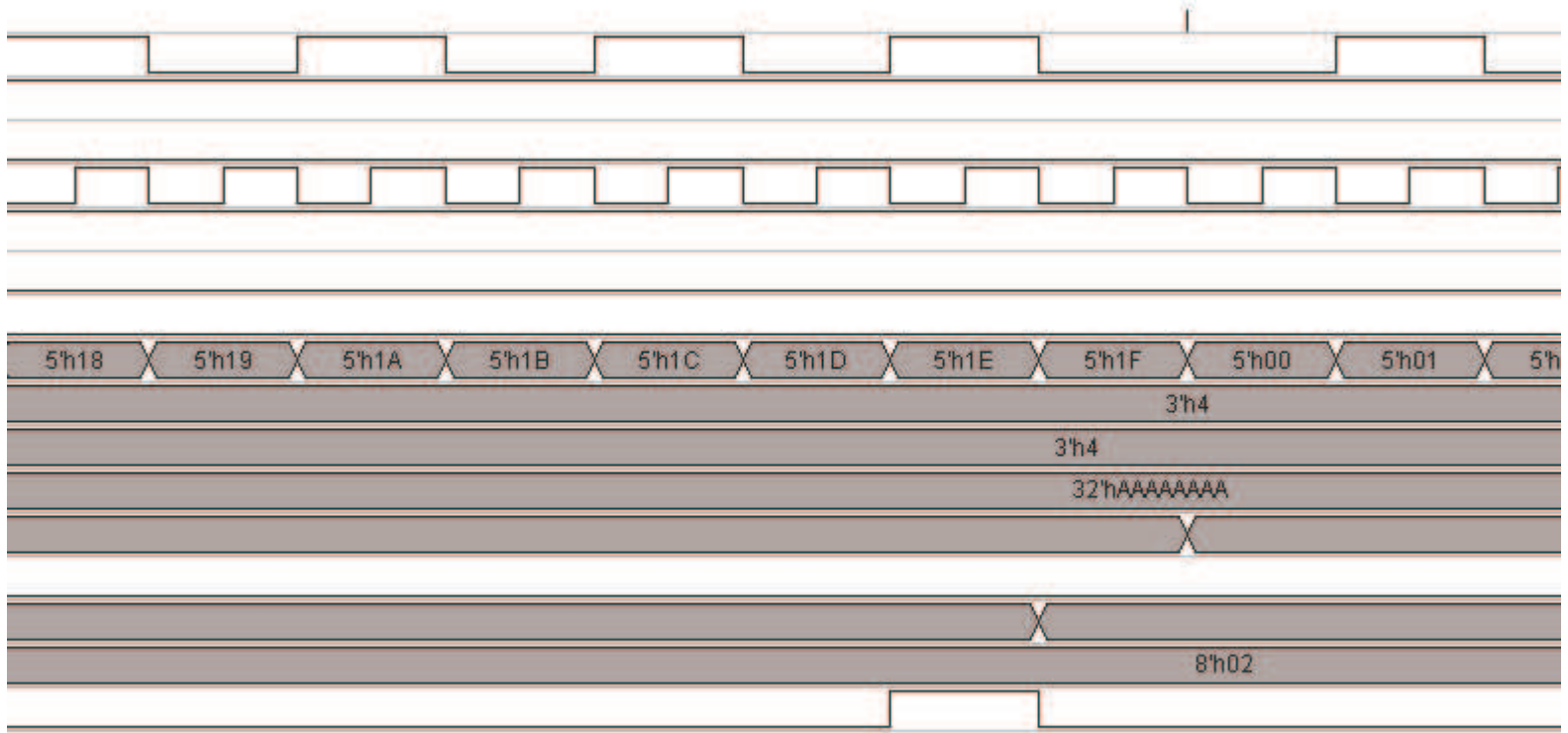


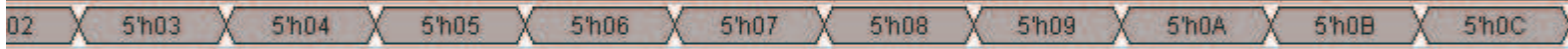


ID	5'h0E	5'h0F	5'h10	5'h11	5'h12	5'h13	5'h14	5'h15	5'h16	5'h17
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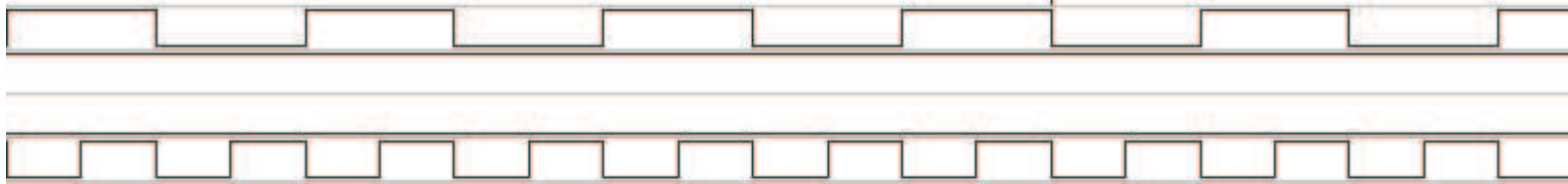
32'h55555555

8'h00





6000



32'hAAAAAAAA

8'h01

