

CMPE 650: Digital Systems Design

Course:

CMPE 650: Digital Systems Design, Spring 2008. 3 credits.

Course Instructor:

Dr. Jim Plusquellic, Professor of Computer Science & Electrical Engineering

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Office Hours: M-W 6:45-7:45pm or by appointment

Text:

Howard Johnson and Martin Graham, "High-Speed Digital Design: A Handbook of Black Magic", Prentice Hall, 1993 (ISBN:0133957241)

Supplementary text:

Stephen H. Hall, Garrett W. Hall and James A. McCall, "High-Speed Digital System Design: A Handbook of Interconnect Theory and Design Practices", Wiley, 2000 (ISBN:0471360902)

Grading:

The distribution of weights for the exams, homeworks and projects is as follows:

Exam 1	25%
Final	30%
Labs/Homework	20%
Project	20%
Class Participation	5%

No incompletes will be given, except as required by university policy for truly exceptional circumstances.

The final exam is cumulative. However, material covered after the second exam will be emphasized.

Students are encouraged to participate in class.

NOTE: Cheating at any time in this course will cause you to fail the course.

Please refer to the guidelines on the next page.

For a complete description of academic dishonesty, refer to the UMBC Student Handbook.

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The following is taken from the UMBC Student Handbook:

DEFINITIONS OF ACADEMIC MISCONDUCT

Academic misconduct may include but is not limited to the following:

Cheating: knowingly using or attempting to use unauthorized material, information, or study aids in any academic exercise.

Fabrication: Intentional and unauthorized falsification or invention of any information or citation in an academic exercise.

Facilitating Academic Dishonesty: Intentionally or knowingly helping or attempting to help another commit an act of academic dishonesty.

Plagiarism: Knowingly representing the words or ideas of another as one's own in any academic exercise, including works of art and computer-generated information/images.

POLICY FOR RESOLVING CASES OF ACADEMIC MISCONDUCT

Individual faculty members have the right and responsibility to deal directly with any cases of academic misconduct which arise in their courses. Instances of academic misconduct may be identified in one of two ways. If a faculty member believes a student has committed an act of academic misconduct--for example, by direct observation of student behavior, by comparing the contents of an assignment with that submitted by another student, or by reviewing notated sources or references--the faculty member, in consultation with the Chair of the Academic Conduct Committee, will assess the student's alleged misconduct and the faculty member's options. If a student believes that academic misconduct has occurred, the student will notify either the faculty member or the Chair of the Academic Conduct Committee.

It is particularly important that the Chair of the Academic Conduct Committee be consulted. The Chair can provide knowledge and insight for the faculty member. Communication of instances of academic misconduct also protects the integrity of the university by providing a means of recording infractions that may be repeated by a particular student, or which may prove endemic to a particular course or department. Consultation with the Chair of the Academic Conduct Committee provides a formal record of the infraction and resolution, protecting the student, professor, and university should any questions later arise.

The student will have the opportunity to respond to an accusation of academic misconduct.

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Tentative Course Outline:

Date	Topic
Week 1	Introduction
	Capacitance and Inductance
Week 2	Capacitance and Inductance
	Power, speed and packages
Week 3	Power, speed and packages
	Power, speed and packages
Week 4	Power, speed and packages
	Power, speed and packages
Week 5	Power, speed and packages
	Scope measurement methods
Week 6	Scope measurement methods
	Transmission Lines
Week 7	Transmission Lines
	Midterm
Week 8	Transmission Lines
	Transmission Lines
Week 9	GND planes and layer stacking
	GND planes and layer stacking
Week 10	Terminations
	Terminations
Week 11	Vias
	Vias
Week 12	Power Distribution Systems
	Power Distribution Systems
Week 13	Connectors and Ribbon Cables
	Connectors and Ribbon Cables
Week 14	Clock Distribution
	Clock Distribution
Week 15	Advanced Topics
	Final exam

(Note: Changes/Additions to this schedule will be posted on my web site
<http://www.cs.umbc.edu/~plusquel/>)